



SPECIFICATION CHANGES MADE IN ADSP-SC582,SC583,SC584,SC587,SC589, 21583, 21584, 21587 DATA SHEET, REVISION B

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Rev A:

Program Trace Macrocell (PTM) Timing

Table 106 and Figure 80 provide I/O timing related to the PTM.

Table 106. Trace Timing

Parameter		Min	Max	Unit
<i>Switching Characteristics</i>				
t _{DTRD}	Trace Data Delay From Trace Clock Maximum		5	ns
t _{HTRD}	Trace Data Hold From Trace Clock Minimum	2		ns
t _{PTRCK}	Trace Clock Period Minimum	12.32		ns

Rev B:

Program Trace Macrocell (PTM) Timing

Table 105 and Figure 79 provide I/O timing related to the PTM.

Table 105. Trace Timing

Parameter		Min	Max	Unit
<i>Switching Characteristics</i>				
t _{DTRD}	Trace Data Delay From Trace Clock Maximum		0.5 × t _{SCLK0} + 2	ns
t _{HTRD}	Trace Data Hold From Trace Clock Minimum	0.5 × t _{SCLK0} – 1.2		ns
t _{PTRCK}	Trace Clock Period Minimum	2 × t _{SCLK0} – 1		ns

FOR THE PCIE POWER SUPPLIES, NEW FOOTNOTE #2 HAS BEEN ADDED:

¹ Guidance also applies to models that do not feature the associated hardware block. See Table 2 or Table 3 for further information.

² For boundary scan to work, PCIE power supplies must be connected as per Specifications.